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17548 U.S.PTO
10/812934



033104

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir: Transmitted herewith for filing is the patent application of:

Inventor: K. MATSUDA et al (see attached)

For:
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:

12 Sheets of Drawings
 This application is being filed without an executed Declaration.

Priority is claimed from Japanese Application No. 2003-119303 (which is hereby filed April 24, 2003). A certified copy is attached herewith. incorporated by reference

Copies of the disclosure documents listed on the attached PTO 1449 form and discussed in the specification or attached Information Disclosure Statement.

Small entity status under 37 CFR 1.9 and 1.27 is hereby claimed.

Specification: Abstract , Description 30 pages; and 7 claim(s).

Preliminary Amendment.

Executed Declaration.

The filing fee is calculated as shown below:

For:	No. Filed	No. Extra
Basic Fee		
Total Claims	7 -20 = * 0	
Indep Claims	2 - 3 = * 0	
<input type="checkbox"/> Multiple Dependent Claim (s)		

* If difference is less than zero
then enter '0' in second column

Small Entity

Rate	Fee
	\$ 375
x 9	\$
x 42	\$
+ 140	\$
Total	\$

OR

Rate	Fee
	\$ 770
x 18	\$ 0
x 84	\$ 0
+ 280	\$ 0
Total	\$ 770

A check or credit card payment form in the amount of \$ 770.00 is enclosed for the filing fee.

The Commissioner is hereby authorized to charge any additional fees that may be required to Deposit Account No. 50-1417.

Respectfully Submitted,

By:

John R. Mattingly
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LIST OF INVENTORS' NAMES AND ADDRESSES

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